I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

11-21-06
_____
Date

~Jennifer A. Steele~
_____
Jennifer A. Steele

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants	: Timothy B. Cowles, Michael A. Shore and Patrick J. Mullarkey	Attorney Docket No.:	500792.05
Patent No.	: US 6,850,457 B2	Serial No.	: 10/043,682
Issue Date	: February 1, 2005	Filed	: January 10, 2002
Title	: REFRESH CONTROLLER AND ADDRESS REMAPPING CIRCUIT AND METHOD FOR DUAL MODE FULL/REDUCED DENSITY DRAMS		

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate
NOV 3 0 2006
of Correction

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

A Certificate of Correction under 35 U.S.C. § 254 is respectfully requested for the above-identified patent in order to correct Patent and Trademark Office errors made during the printing of the patent. The changes in the patent needed to correct the errors are as follows:

Column, Line	Reads	Should Read
Item (75), Line 1	"Timoty B. Cowles,"	--Timothy B. Cowles,--
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Column 1, Line 56	"in the art. DRAM memory"	--in the art, DRAM memory--
Column 2, Line 2	"logic level of can no longer"	--logic level can no longer--
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Column 2, Line 14	"problems is disclosed"	--problem is disclosed--
Column 2, Line 21	"complimentary"	--complementary--
Column 2, Line 24	"to store in"	--to store--
Column 2, Lines 65 and 67	"DRAM's"	--DRAMs--
Column 3, Line 10	"command are applied"	--commands are applied--
Column 4, Line 7	"receiving a time-"	--receiving time- --
Column 4, Line 34	"applied the"	--applied to the--
Column 4, Line 49	"RAO is high,"	--RA0 is high,--
Column 5, Line 14	"for clarity, it will be understood"	--for clarity. It will be understood--
Column 5, Line 20	"DO"	--D0--
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Column 5, Line 37	"then energized"	--then energizes--
Column 6, Line 35	"DO"	--D0--
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Column 7, Lines 3 and 7-8	"complimentary"	--complementary--
Column 7, Line 5	"DO and its compliment DO*"	--D0 and its complement D0*--
Column 7, Lines 22 and 24	"VT"	--V_T--
Column 7, Lines 35 and 48	"compliment"	--complement--

Column 7, Line 39	"only one digit lines"	--only one digit line--
Column 7, Lines 57-58	"each Memory Address"	--each memory address--
Column 8, Line 5	"one of the row address"	--one of the row addresses--
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The above errors for which correction is requested under 35 U.S.C. § 254 were made in the printing of the patent or in the original application. The errors are considered sufficiently important to justify the processing of a Certificate of Correction under 35 U.S.C. § 254. A Form PTO-1050, in duplicate, is enclosed herewith.

The Commissioner is hereby authorized to charge payment of any fees associated with this communication to Deposit Account No. 50-1266. A duplicate copy of this sheet is enclosed.

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Favorable consideration of this Request is respectfully requested.

Respectfully submitted,

Date: _Nov. 20, 2006_   By: _Edward W. Bulchis_

Edward W. Bulchis, Reg. No. 26,847
Customer No. 27,076
Dorsey & Whitney LLP
1420 Fifth Avenue, Suite 3400
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UNITED STATES PATENT AND TRADEMARK OFFICE
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PATENT NO.   :   US 6,850,457 B2
DATED   :   February 1, 2005
INVENTOR(S)   :   Timothy B. Cowles, Michael A. Shore and Patrick J. Mullarkey

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MAILING ADDRESS OF SENDER:

DORSEY & WHITNEY LLP
1420 Fifth Avenue, Suite 3400
Seattle, Washington 98101

FORM PTO-1050 (REV. 3-82)

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Patent No. US 6,850,457 B2

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Attorney for Applicant(s)

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